Prof. Dr. Jan-Georg Smaus
98 chemin de Lanusse, D305
31200 Toulouse
France
Phone: +33.5.6155 6304
E-mail: smaus@irit.fr

Curriculum Vitae

Personal Data

Name	Jan-Georg Smaus
Place of Birth	Saarbrücken, Germany
Date of Birth	5th July 1970
Nationality	French and German
Family Status	Married, two children

Employment

09/2011–present Professor at the Computer Science Insititute (IRIT) of Paul Sabatier University of Toulouse

10/2010–7/2011 Interim professor at the Research Group on the Foundations of Artificial Intelligence (sabbatical substitute for Prof. Dr. Bernhard Nebel)

07/2004–8/2011: Employed as Scientific Assistant (wissenschaftlicher Assistent) at the University of Freiburg in the project AVACS: Automatic Verification and Analysis of Complex Systems. I am a member of the Research Group on the Foundations of Artificial Intelligence of **Prof. Dr. Bernhard Nebel**.

06/2001–04/2004: Employed as Scientific Assistant (wissenschaftlicher Assistent) at the University of Freiburg in the project MMiSS: Multimedia Instruction in Secure Systems. I was a member of the Software Engineering Group of **Prof. Dr. David Basin**.

10/1999–05/2001: ERCIM fellow in the Postdoctoral Fellowship Programme. First, I was at INRIA-Rocquencourt, working for **Prof. Pierre Deransart**. From July 2000, I was at CWI Amsterdam, working for **Prof. Krzysztof Apt**.

3/1996–8/1999: Employed at the University of Kent at Canterbury as a Research Associate/Postgraduate student under a project grant by the British Engineering and Physical Sciences Research Council (EPSRC). The project titled "Detecting and Exploiting Determinacy in Logic Programs" was a joint project of the University of Kent at Canterbury (grant holder **Dr. Andy King**) and the University of Leeds (grant holder **Dr. Pat Hill**).

Teaching

In July 2010 I received the **Teaching Award** 2010 of the Faculty of Engineering at Freiburg University, endowed with prize money of 2000 Euros. The award is based on nominations by the students.

University of Toulouse

Title	Level	Nature	Hours	Taught in
Logic Maths/Info puis Outils Maths Discrèts	L	CTD		

Stopped here in 2020/01. Too tedious, dossier for Avancement Grade has priority!

Explanations:
Level: **L**icence (bachelor) or **M**aster
All teaching is in initial training and face-to-face.
Nature: **C**ours **M**agistral (lecture), **T**ravaux **D**irigés (paper-and-pencil exercises), CTD (combination of the two above), **T**ravaux **P**ratiques (computer exercises)
CMs usually have more than 100 participants, TDs have appr. 36 participants, and TPs have appr. participants.
The number of hours is per year.
S1 and **S2** refer to the semesters starting in September and January, respectively.

ENSEEIHT Toulouse

ENAC Toulouse

University of Albi

University of Freiburg

Lectures (Vorlesungen, Cours, Cours/TD)

- Lecture Logic (cours/TD), given given at the University of Toulouse in the winter semester of 11/12.

- Lecture Math/Computer Science (cours/TD), given given at the University of Toulouse in the winter semester of 11/12.

- Lecture (French: cours/TD) Computer Science I, given given at the University of Toulouse in the winter semester of 11/12.

- Lecture Computer Science Theory I, given given at the University of Freiburg in the summer semester of 11.

- Lecture Computer Science I, given given at the University of Freiburg in the winter semester of 10/11.

- Lecture on Computer Supported Modeling and Reasoning, given at the University of Freiburg in each winter semester since 03/04.

- Lecture "Pearls of Computer-Supported Modeling and Reasoning", given at the Università degli Studi dell'Aquila (Italy) in March 2010 within the framework of the Erasmus lecturer exchange programme.

Seminars

- Organisation of the seminar on Self Reference held at the chair for the Foundations of Artificial Intelligence at the University of Freiburg in the summer semester of 2009.

- Collaboration in various seminars organised by the Research Group on the Foundations of Artificial Intelligence.

Practicals

- Collaboration in a Practical on Artificial Intelligence held at the chair for the Foundations of Artificial Intelligence at the University of Freiburg in the summer semester of 2009 (my topics were Constraint Programming and Natural Language)

Labs

- Labs of the lecture on Computer Supported Modeling and Reasoning in the winter semesters of 01/02 and 02/03.

- Course assistance in courses on functional programming, electronic publishing, and structured programming, at the University of Kent at Canterbury in the years 1996 through 1999.

- Course assistance in an introductory course in computer science and in a course on logic.

Supervision

- Alexander Schimpf: PhD in the area of interactive theorem proving and model checking, starting in October 2010

- Christian Schilling: Bachelor Thesis "Solving the threschold synthesis problem of Boolean functions by translation to linear programming", August 2011

- Fabian Wenzelmann: Bachelor Thesis "Solving the threschold synthesis problem of Boolean functions by a combinatorial algorithm", August 2011

- Kai Siebold: Diplomarbeit "Transformation of propositional formulae into linear pseudo-Boolean constraints", October 2010

- Alexander Schimpf: Diplomarbeit "Implementierung eines Verfahrens zur Erzeugung von Büchi-Automaten aus LTL-Formeln in Isabelle" ("Implementation of a procedure for generating Büchi-automata from LTL formulae in Isabelle"), 12/2008

- Jan Rehm: Diplomarbeit "Zufälliges Erstellen von Realzeit-Automaten im Uppaal-Format" ("Random generation of timed automata in the Uppaal format"), 02/2008

Projects

- Acquisition of a three-year research grant (one PhD position, one student assistant position) for the project "Computer-aided verification of automata constructions for model checking (CAVA)" by the Deutsche Forschungsgemeinschaft

- Collaboration in the administration of the MMiSS and AVACS projects.

- Collaboration in the writing of the successful follow-up proposal of the AVACS project.

Conference Organisation

2012   *German Conference on Artificial Intelligence (KI 2012)*: Programme committee member

2012   *Conference on Artificial Intelligence (AAAI 12)*: Programme committee member

2011   *German Conference on Artificial Intelligence (KI 2011)*: Programme committee member

2010   *Workshop on Model Checking and Artificial Intelligence*: Co-chair

2010   *International Symposium on Functional and Logic Programming*: Programme committee member

2007-08   *ACM Symposium on Applied Computing*, Software-Track: Programme committee member

2003-05   Workshop *Logic Programming Environments*: Programme committee member

2003   *International Conference on Logic Programming*: Publicity chair

1999   *Workshop on Verification of Logic Programs*: Co-chair

Refereeing

- Conferences: AAAI, ACM SAC, CAV, CL'00, ECAI, ESOP, FLOPS, ICALP, ICLP, IDPS, KI, LOPSTR, PPDP, SAS, VMCAI, WFLP, and WLPE

- Journals: AAECC, AI, Fundamenta Informaticae, IDPS, IET Software, IPL, JAR, JLC, JLP, SCP, TCS, TOCL, TOPLAS, and TPLP

Education

7/2009   Habilitation and Venia Legendi at the University of Freiburg. Title of the Habilitationsschrift: "Logic and Abstraction, Verification and Falsification".

7/2000   PhD in Computer Science at the University of Kent at Canterbury. Title of the PhD thesis: "Modes and Types in Logic Programming"

2/1996   Diplom (Master's degree) in Computer Science at the University of the Saarland (1990-1996). Title of the Diplom thesis: "Resolution K-Transformations". Minor subject: Economics.

7/1989   Abitur

Other Experiences

Civil service

7/1989 – 9/1990 Civil service (as a replacement of military service), providing home care for severely disabled people

Languages

Apart from German and English, I have a good knowledge of French, Italian and Dutch, and a fair knowledge of Czech.

Publications

Journal Articles

[1] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Verifying termination and error-freedom of logic programs with `block` declarations. *Theory and Practice of Logic Programming*, 1(4):447–486, 2001.

[2] Dino Pedreschi, Salvatore Ruggieri, and Jan-Georg Smaus. Classes of terminating logic programs. *Theory and Practice of Logic Programming*, 2(3):369–418, 2002.

[3] Pierre Deransart and Jan-Georg Smaus. Subject reduction of logic programs as proof-theoretic property. *Journal of Functional and Logic Programming (electronic journal)*, 2002(2), 2002.

[4] Annalisa Bossi, Sandro Etalle, Sabina Rossi, and Jan-Georg Smaus. Termination of simply moded logic programs with dynamic scheduling. *Transactions on Computational Logic*, 5(3):470–507, 2004.

Conference Papers

[5] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Termination of logic programs with `block` declarations running in several modes. In Catuscia Palamidessi, editor, *Proceedings of the 10th Symposium on Programming Language Implementations and Logic Programming*, volume 1490 of *LNCS*, pages 73–88. Springer-Verlag, 1998.

[6] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Preventing instantiation errors and loops for logic programs with multiple modes using `block` declarations. In Pierre Flener, editor, *Proceedings of the 8th International Workshop on Logic-based Program Synthesis and Transformation*, volume 1559 of *LNCS*, pages 289–307. Springer-Verlag, 1999.

[7] Andy King, Jan-Georg Smaus, and Patricia M. Hill. Quotienting *Share* for dependency analysis. In Doaitse Swierstra, editor, *Proceedings of the 8th European Symposium on Programming*, volume 1576 of *LNCS*, pages 59–73. Springer-Verlag, 1999.

[8] Jan-Georg Smaus. Proving termination of input-consuming logic programs. In Danny De Schreye, editor, *Proceedings of the 16th International Conference on Logic Programming*, pages 335–349. MIT Press, 1999.

[9] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Mode analysis domains for typed logic programs. In Annalisa Bossi, editor, *Proceedings of the 9th International Workshop on Logic-based Program Synthesis and Transformation*, volume 1817 of *LNCS*, pages 83–102, 2000.

[10] Jan-Georg Smaus and Pierre Deransart. Les programmes bien typés ont tout bon. In Touraïvane, editor, *Proceedings of Journées Francophones de Programmation en Logique avec Contraintes*, pages 49–65. Hermes Science Publications, 2000.

[11] Jan-Georg Smaus, François Fages, and Pierre Deransart. Using modes to ensure subject reduction for typed logic programs with subtyping. In Sanjiv Kapoor

and Sanjiva Prasad, editors, *Proceedings of the 20th Conference on the Foundations of Software Technology and Theoretical Computer Science*, volume 1974 of *LNCS*, pages 214–226. Springer-Verlag, 2000.

[12] Pierre Deransart and Jan-Georg Smaus. Well-typed logic programs are not wrong. In Herbert Kuchen and Kazunori Ueda, editors, *Proceedings of the 5th International Symposium on Functional and Logic Programming*, volume 2024 of *LNCS*, pages 280–295. Springer-Verlag, 2001.

[13] Annalisa Bossi, Sandro Etalle, Sabina Rossi, and Jan-Georg Smaus. Semantics and termination of simply-moded logic programs with dynamic scheduling. In David Sands, editor, *Proceedings of the European Symposium on Programming*, volume 2028 of *LNCS*, pages 402–416. Springer-Verlag, 2001.

[14] Jan-Georg Smaus. Analysis of polymorphically typed logic programs using ACI-unification. In Robert Nieuwenhuis and Andrei Voronkov, editors, *Proceedings of the 8th International Conference on Logic for Programming, Artificial Intelligence and Reasoning*, volume 2250 of *LNAI*, pages 280–295. Springer-Verlag, 2001.

[15] Jan-Georg Smaus. The head condition and polymorphic recursion. In Zhenjiang Hu and Mario Rodríguez-Artalejo, editors, *Proceedings of the 6th International Symposium on Functional and Logic Programming*, volume 2441 of *LNCS*, pages 259–274. Springer-Verlag, 2002.

[16] Jan-Georg Smaus. Is there an optimal generic semantics for first-order equations? In Catuscia Palamidessi, editor, *Proceedings of the 19th International Conference on Logic Programming*, volume 2916 of *LNCS*, pages 438–450. Springer-Verlag, 2003.

[17] Jan-Georg Smaus. Termination of logic programs using various dynamic selection rules. In Bart Demoen and Vladimir Lifschitz, editors, *Proceedings of the 20th International Conference on Logic Programming*, volume 3132 of *LNCS*, pages 43–57. Springer-Verlag, 2004.

[18] Stefan Ratschan and Jan-Georg Smaus. Verification-integrated falsification of non-deterministic hybrid systems. In Christos G. Cassandras, Alessandro Giua, Carla Seatzu, and Janan Zaytoon, editors, *Proceedings of the 2nd (2006) IFAC Conference on Analysis and Design of Hybrid Systems*, pages 371–376. Elsevier Science Inc., 2007.

[19] Jörg Hoffmann, Jan-Georg Smaus, Andrey Rybalchenko, Sebastian Kupferschmid, and Andreas Podelski. Using predicate abstraction to generate heuristic functions in UPPAAL. In Stefan Edelkamp and Alessio Lomuscio, editors, *Post-Proceedings of the 4th (2006) Workshop on Model Checking and Artificial Intelligence*, volume 4428 of *LNCS*, pages 51–66. Springer-Verlag, 2007.

[20] Jan-Georg Smaus. On Boolean functions encodable as a single linear pseudo-Boolean constraint. In Pascal Van Hentenryck and Laurence Wolsey, editors, *Proceedings of the 4th International Conference on Integration of AI and OR Techniques in Constraint Programming for Combinatorial Optimization Problems*, volume 4510 of *LNCS*, pages 288–302. Springer-Verlag, 2007.

[21] Jan-Georg Smaus and Jörg Hoffmann. Relaxation refinement: A new method to generate heuristic functions. In Doron Peled and Michael Wooldridge, editors, *Post-Proceedings of the 5th (2008) Workshop on Model Checking and Artificial Intelligence*, volume 5348 of *LNCS*, pages 147–165. Springer-Verlag, 2009.

[22] Stefan Ratschan and Jan-Georg Smaus. Finding errors of hybrid systems by optimising an abstraction-based quality estimate. In Catherine Dubois, editor, *Proceedings of the 3rd International Conference on Tests and Proofs*, volume 5668 of *LNCS*, pages 153–168. Springer-Verlag, 2009.

[23] Alexander Schimpf, Stephan Merz, and Jan-Georg Smaus. Construction of Büchi automata for LTL model checking verified in Isabelle/HOL. In Stefan Berghofer, Tobias Nipkow, Christian Urban, and Makarius Wenzel, editors, *Proceedings of the 22nd International Conference on Theorem Proving in Higher Order Logics*, volume 5674 of *LNCS*, pages 424–439. Springer-Verlag, 2009.

[24] Evgeny Dantsin, Jan-Georg Smaus, and Sergei Soloviev. Algorithms in games evolving in time: Winning strategies based on testing hypotheses. In Tobias Nipkow, Larry Paulson, and Makarius Wenzel, editors, *Isabelle Users Workshop of the International Conference on Interactive Theorem Proving*, 2012.

[25] Jan-Georg Smaus, Christian Schilling, and Fabian Wenzelmann. Implementations of two algorithms for the threshold synthesis problem. In *International Symposium on Artificial Intelligence and Mathematics, ISAIM 2012, Fort Lauderdale, Florida, USA, January 9-11, 2012*, 2012.

[26] Christian Schilling, Jan-Georg Smaus, and Fabian Wenzelmann. A pretty complete combinatorial algorithm for the threshold synthesis problem. In Thierry Lecroq and Laurent Mouchard, editors, *Combinatorial Algorithms - 24th International Workshop, IWOCA 2013, Rouen, France, July 10-12, 2013, Revised Selected Papers*, volume 8288 of *Lecture Notes in Computer Science*, pages 458–462. Springer, 2013.

[27] Javier Esparza, Peter Lammich, René Neumann, Tobias Nipkow, Alexander Schimpf, and Jan-Georg Smaus. A fully verified executable LTL model checker. In Natasha Sharygina and Helmut Veith, editors, *Computer Aided Verification - 25th International Conference, CAV 2013, Saint Petersburg, Russia, July 13-19, 2013. Proceedings*, volume 8044 of *LNCS*, pages 463–478. Springer-Verlag, 2013.

[28] Nadezhda Baklanova, Wilmer Ricciotti, Jan-Georg Smaus, and Martin Strecker. Abstracting an operational semantics to finite automata. In Sotiris Batsakis, Heinrich C. Mayr, Vitaliy Yakovyna, Mykola Nikitchenko, Grygoriy Zholtkevych, Vyacheslav Kharchenko, Hennadiy Kravtsov, Vitaliy Kobets, Vladimir S. Peschanenko, Vadim Ermolayev, Yuriy Bobalo, and Aleksander Spivakovsky, editors, *Proceedings of the 11th International Conference on ICT in Education, Research and Industrial Applications: Integration, Harmonization and Knowledge Transfer, Lviv, Ukraine, May 14-16, 2015.*, volume 1356 of *CEUR Workshop Proceedings*, pages 354–365. CEUR-WS.org, 2015.

[29] Alexander Schimpf and Jan-Georg Smaus. Büchi automata optimisations formalised in isabelle/hol. In Mohua Banerjee and Shankara Narayanan Krishna, editors, *Logic and Its Applications - 6th Indian Conference, ICLA 2015, Mumbai, India, January 8-10, 2015. Proceedings*, volume 8923 of *Lecture Notes in Computer Science*, pages 158–169. Springer, 2015.

[30] Stéphane Le Roux, Érik Martin-Dorel, and Jan-Georg Smaus. An existence theorem of nash equilibrium in Coq and Isabelle. In Patricia Bouyer, Andrea Orlandini, and Pierluigi San Pietro, editors, *Proceedings Eighth International Symposium on Games, Automata, Logics and Formal Verification, GandALF 2017, Roma, Italy, 20-22 September 2017.*, volume 256 of *EPTCS*, pages 46–60, 2017.

Reviewed Articles in Special Volumes

[31] Dino Pedreschi, Salvatore Ruggieri, and Jan-Georg Smaus. Characterisations of termination in logic programming. In Maurice Bruynooghe and Kung-Kiu Lau, editors, *Program Development in Computational Logic*, volume 3049 of *LNCS*, pages 376–431. Springer-Verlag, 2004.

Editorship

[32] Sandro Etalle and Jan-Georg Smaus, editors. *Proceedings of the Workshop on Verification of Logic Programs held at ICLP'99*, volume 30(1) of *Electronic Notes in Theoretical Computer Science*. Elsevier, 1999.

[33] Ron van der Meyden and Jan-Georg Smaus, editors. *Model Checking and Artificial Intelligence - 6th International Workshop, MoChArt 2010, Atlanta, GA, USA, July 11, 2010, Revised Selected and Invited Papers*, volume 6572 of *Lecture Notes in Computer Science*. Springer, 2011.

Reviewed Workshop Papers

[34] Jan-Georg Smaus and Pierre Deransart. Well-typed programs are not wrong. In Michael Leuschel et al., editor, *Verification and Computational Logic Workshop*. Technical report DSEE-TR-2000-6 of the University of Southampton, 2000. Held within CL'2000.

[35] Jan-Georg Smaus. Representing Boolean functions as linear pseudo-Boolean constraints. In Youssef Hamadi, editor, *Proceedings of the CP 2006 Workshop on the Integration of SAT and CP techniques*, pages 49–63, 2006.

[36] Stefan Ratschan and Jan-Georg Smaus. Finding errors of hybrid systems by optimising an abstraction-based quality estimate. In Tarmo Uustalu and Jüri Vain, editors, *Proceedings of the 20th Nordic Workshop on Programming Theory*, pages 72–74, 2008.

[37] Bahareh Badban, Stefan Leue, and Jan-Georg Smaus. Automated invariant generation for the verification of real-time systems. In Andrei Voronkov, Laura Kovács, and Nikolaj Bjørner, editors, *Second International Workshop on Invariant Generation, WING 2009, York, UK, March 29, 2009 and Third International Workshop on Invariant Generation, WING 2010, Edinburgh, UK, July 21, 2010*, volume 1 of *EPiC Series in Computing*, pages 44–58. EasyChair, 2010.

Invited Contributions

[38] Krzysztof R. Apt and Jan-Georg Smaus. Rule-based versus procedure-based view of logic programming. *Joint Bulletin of the Novosibirsk Computing Center and Institute of Informatics Systems; Series: Computer Science*, 16:75–97, 2001.

[39] Bernd Krieg-Brückner, Dieter Hutter, Arne Lindow, Christoph Lüth, Achim Mahnke, Erica Melis, Philipp Meier, Arnd Poetzsch-Heffter, Markus Roggenbach, George Russell, Jan-Georg Smaus, and Martin Wirsing. Multimedia instruction in safe and secure systems. In Martin Wirsing, Dirk Pattinson, and Rolf Henecker, editors, *Recent Trends in Algebraic Development Techniques, 16th International Workshop, WADT 2002*, volume 2755 of *LNCS*, pages 82–117. Springer-Verlag, 2003.

Reviewed Posters

[40] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Domain construction for mode analysis of typed logic programs. In Lee Naish, editor, *Proceedings of the*

14th Joint International Conference and Symposium on Logic Programming, page 418. MIT Press, 1997. Abstract of Poster Presentation.

[41] Jan-Georg Smaus. Termination of logic programs for various dynamic selection rules. In Catuscia Palamidessi, editor, *Proceedings of the 19th International Conference on Logic Programming*, volume 2916 of *LNCS*, pages 511–512. Springer-Verlag, 2003. Abstract of Poster Presentation.

Theses

[42] Jan-Georg Smaus. Resolution K-transformations. Diplomarbeit, Universität des Saarlandes (Max-Planck-Institut für Informatik), 1996.

[43] Jan-Georg Smaus. *Modes and Types in Logic Programming*. PhD thesis, University of Kent at Canterbury, 1999.

[44] Jan-Georg Smaus. *Logic and Abstraction, Verification and Falsification*. Habilitationsschrift, Albert-Ludwigs-Universität Freiburg, 2009. Consists of a 51-page Summary and the articles [2, 3, 4, 11, 13, 14, 15, 16, 17, 18, 19, 20, 21, 31, 35].

Technical Reports

[45] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Domain construction for mode analysis of typed logic programs. Technical Report 8-97, Computing Laboratory, University of Kent at Canterbury, United Kingdom, 1997. Early version of [9].

[46] Andy King, Jan-Georg Smaus, and Patricia M. Hill. Practical dependency analysis through a $Share$ quotient. Technical Report 11-98, Computing Laboratory, University of Kent, 1998. Early version of [7].

[47] Jan-Georg Smaus. Well-terminating, input-driven logic programs. Technical Report 16-98, University of Kent at Canterbury, 1998. Early version of [8].

[48] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Verification of logic programs with `block` declarations running in several modes. Technical Report 7-98, University of Kent at Canterbury, Canterbury, CT2 7NF, United Kingdom, July 1998. Contains proofs for [5, 6]; see also [1].

[49] Jan-Georg Smaus. Proving termination of input-consuming logic programs. Technical Report 10-99, Computing Laboratory, University of Kent at Canterbury, United Kingdom, 1999. Long version of [8].

[50] Jan-Georg Smaus, Patricia M. Hill, and Andy King. Mode analysis domains for typed logic programs. Technical Report 2000.06, University of Leeds, 2000. Long version of [9].

[51] Jan-Georg Smaus, François Fages, and Pierre Deransart. Using modes to ensure subject reduction for typed logic programs with subtyping. Technical Report RR-4020, INRIA, 2000. Long version of [11], available via CoRR: `http://arXiv.org/archive/cs/intro.html`.

[52] Pierre Deransart and Jan-Georg Smaus. Well-typed logic programs are not wrong. Technical Report RR-4082, INRIA, 2000. Long version of [12], available via CoRR: `http://arXiv.org/archive/cs/intro.html`.

[53] Annalisa Bossi, Sandro Etalle, Sabina Rossi, and Jan-Georg Smaus. On the semantics and termination of logic programs with dynamic scheduling. Technical Report CS2000-11, Università Ca' Foscari di Venezia, 2000. Early version of [13].

[54] Annalisa Bossi, Sandro Etalle, Sabina Rossi, and Jan-Georg Smaus. Semantics and termination of simply-moded logic programs with dynamic scheduling. Long version of [13], available via CoRR: `http://arXiv.org/archive/cs/intro.html`, 2001.

[55] Jan-Georg Smaus. Analysis of polymorphically typed logic programs using ACI-unification. Long version of [14], available via CoRR: `http://arXiv.org/archive/cs/intro.html`, 2001.

[56] Jan-Georg Smaus. Termination of logic programs for various dynamic selection rules. Technical Report 191, Institut für Informatik, Universität Freiburg, 2003.

[57] Jan-Georg Smaus. Termination of logic programs using various dynamic selection rules. Technical Report 203, Institut für Informatik, Universität Freiburg, 2004. Long version of [17].

[58] Jan-Georg Smaus. Representing Boolean functions as linear pseudo-Boolean constraints. Technical Report 227, Institut für Informatik, Universität Freiburg, 2006. Long version of [35].

[59] Jan-Georg Smaus. On Boolean functions encodable as a single linear pseudo-Boolean constraint. Technical Report 230, Institut für Informatik, Universität Freiburg, 2007. Long version of [20]. Also available as TR No. 13 on `http://www.avacs.org/`.

[60] Stefan Ratschan and Jan-Georg Smaus. Finding errors of hybrid systems by optimising an abstraction-based quality estimate. Reports of SFB/TR 14 AVACS 51, SFB/TR 14 AVACS, 2009. ISSN: 1860-9821, http://www.avacs.org. Long version of [22].

[61] Nadezhda Baklanova, Wilmer Ricciotti, Jan-Georg Smaus, and Martin Strecker. Abstracting an operational semantics to finite automata. *CoRR*, abs/1409.7841, 2014.

[62] Javier Esparza, Peter Lammich, René Neumann, Tobias Nipkow, Alexander Schimpf, and Jan-Georg Smaus. A fully verified executable LTL model checker. *Archive of Formal Proofs*, 2014, 2014.

Freiburg, 28th January 2020